



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Name of Applicant: SUN MICROSYSTEMS, INC.  
Address of Applicant: 4150 Network Circle  
Santa Clara, CA 95054

Assistant Commissioner for Patents  
Washington, D.C. 20231

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**REVOCATION AND SUBSTITUTION  
OF POWER OF ATTORNEY UNDER 37 C.F.R. §1.36**

Dear Sir:

Pursuant to 37 C.F.R. §1.36, Sun Microsystems, Inc., the Applicant of the sixteen (16) patent applications listed on the attached APPENDIX A, revokes all previous powers of attorney in the sixteen (16) patent applications listed on the attached APPENDIX A, hereby appoints the following attorneys and/or agents assigned to the Customer Number listed below to prosecute the sixteen (16) patent applications listed on the attached APPENDIX A, and to transact all business in the U.S. Patent and Trademark Office connected therewith:

Customer Number 32615

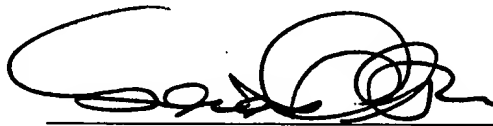
Please direct all correspondence and telephone calls concerning the sixteen (16) patent applications listed on the attached APPENDIX A to:

Jonathan P. Osha  
**ROSENTHAL & OSHA L.L.P.**  
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Houston, Texas 77010  
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The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the above-identified Applicant.

The undersigned hereby declares that all statements made herein of the undersigned's own knowledge are true, that all statements made on information and belief are believed to be true, and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the sixteen (16) patent applications listed on the attached APPENDIX A or any patents issuing thereon.

Signature:



Dated:

August 1, 2002

Name: Kenneth Olsen  
Title: Vice President  
Intellectual Property Law  
Sun Microsystems, Inc.

## **APPENDIX A**

1. Provisional Application No. 60/305,995

Applicant(s): Jeffrey B. Rubin, et al.

Title: Method And Apparatus To Support Threads In A Computing Architecture For  
Logic Simulation And Emulation

Filing Date: July 16, 2001

2. Provisional Application No. 60/306,118

Applicant(s): Thomas M. McWilliams

Title: Bit-Field Processing Architecture For Logic Simulation And Emulation

Filing Date: July 16, 2001

3. Provisional Application No. 60/305,999

Applicant(s): Thomas M. McWilliams

Title: Method And Apparatus For Enhancing Code Density In A Processor  
Architecture

Filing Date: July 16, 2001

4. Provisional Application No. 60/306,122

Applicant(s): Thomas M. McWilliams, et al.

Title: Method And Apparatus For Caching With Variable Size Locking Regions

Filing Date: July 16, 2001

5. Provisional Application No. 60/313,763  
Applicant(s): Thomas M. McWilliams, et al.  
Title: Routing, Placement, and Control For A Simulation System  
Filing Date: August 20, 2001
6. Provisional Application No. 60/305,997  
Applicant(s): Liang T. Chen, et al.  
Title: Multi-Clock System Simulation  
Filing Date: July 16, 2001
7. Provisional Application No. 60/313,762  
Applicant(s): Liang T. Chen, et al.  
Title: Phaser – Compiler Related Inventions  
Filing Date: August 20, 2001
8. Provisional Application No. 60/313,735  
Applicant(s): Thomas M. McWilliams, et al.  
Title: System Related Inventions  
Filing Date: August 20, 2001
9. Provisional Application No. 60/313,217  
Applicant(s): Thomas M. McWilliams, et al.  
Title: Phaser System Architecture  
Filing Date: August 17, 2001

10. Utility Patent Application No. 09/850,366

Applicant(s): Michael W. Parkin

Title: Source Synchronous IO Without Synchronizers Using Temporal Delay Queues

Filing Date: May 7, 2001

11. Utility Patent Application No. 10/020,731

Applicant(s): Nyles I. Nettleton

Title: Use Of Carbon Nanotubes As Electrical Resistors

Filing Date: October 30, 2001

12. Utility Patent Application No. 10/017,668

Applicant(s): Nyles I. Nettleton

Title: Carbon Nanotubes As A Strain Gauge

Filing Date: October 30, 2001

13. Utility Patent Application No. 09/943,209

Applicant(s): William K. Lam, et al.

Title: Method And Apparatus For Automatically Isolating Minimal Distinguishing  
Stimuli In Design Verification And Software Development

Filing Date: August 29, 2001

14. Utility Patent Application No. 10/016,758

Applicant(s): Nyles I. Nettleton

Title: Carbon Nanoloop

Filing Date: October 30, 2001

15. Utility Application No. 10/021,234

Applicant(s): Nyles I. Nettleton

Title: Use of Carbon Nanoloop As An Electronic Device

Filing Date: October 30, 2001

16. Utility Application No. 09/990,935

Applicant(s): William K. Lam

Title: Method And Apparatus For Amortizing Critical Path Computations

Filing Date: November 15, 2001

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